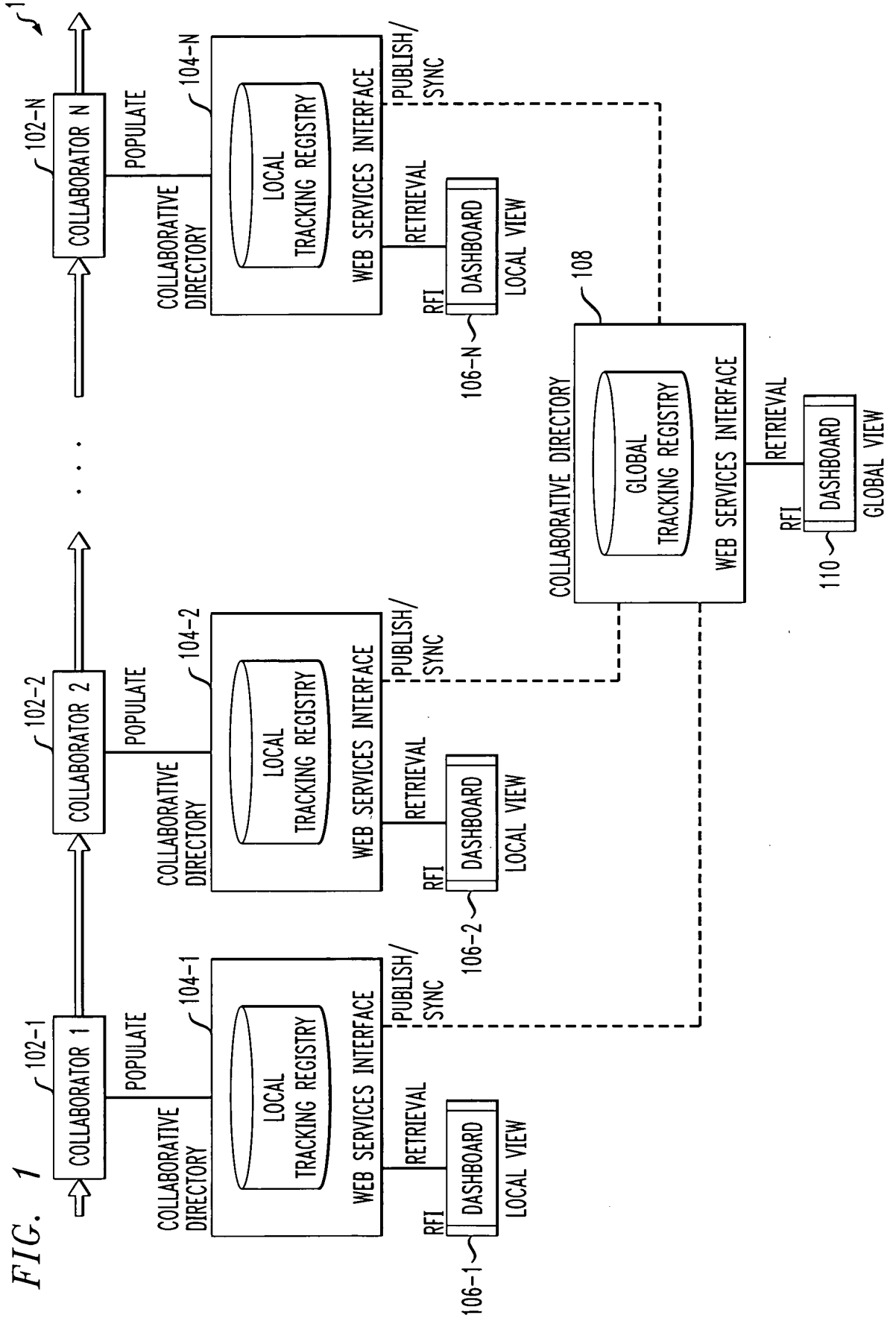


FIG. 1

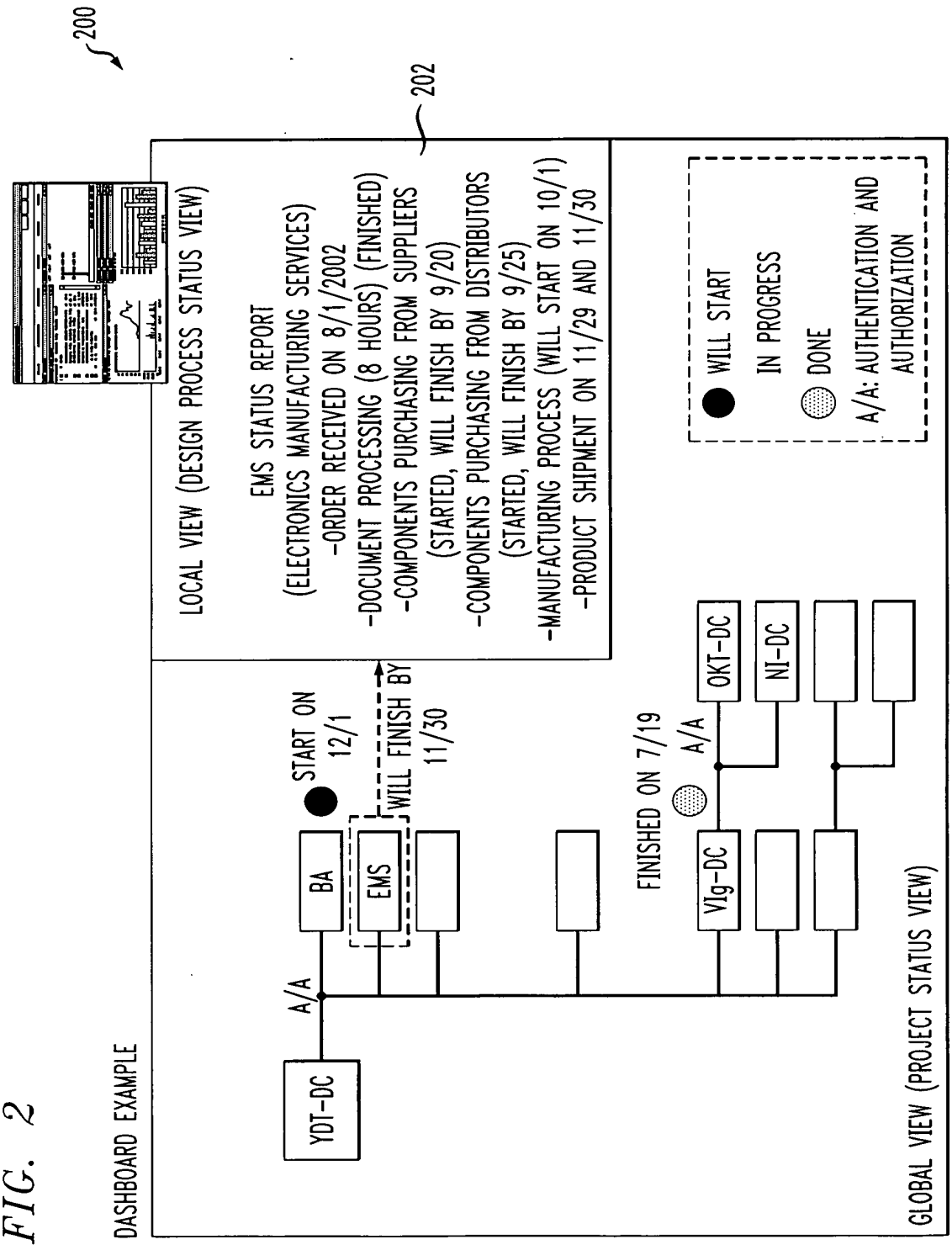


FIG. 2

FIG. 3

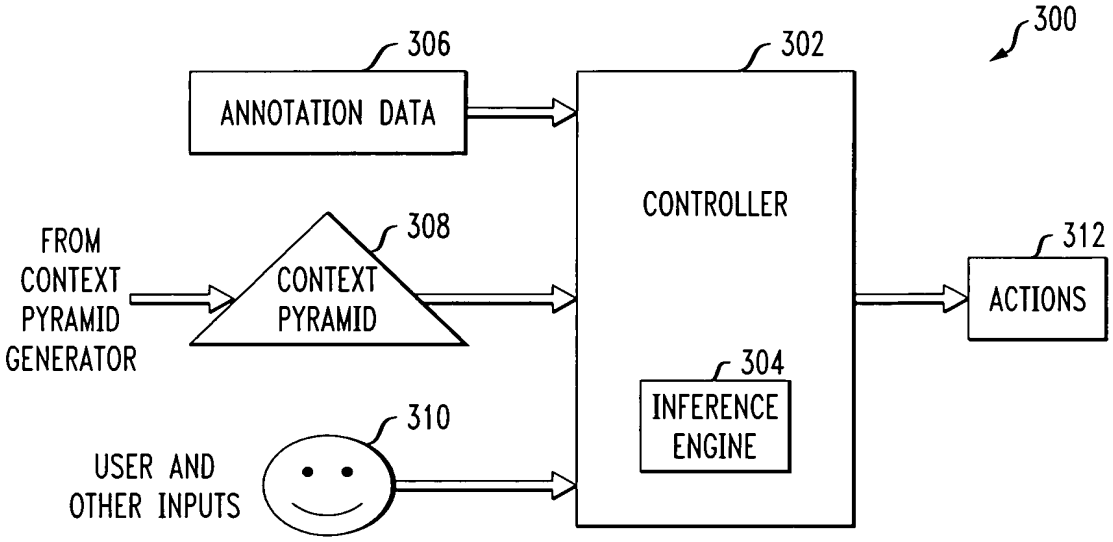


FIG. 4

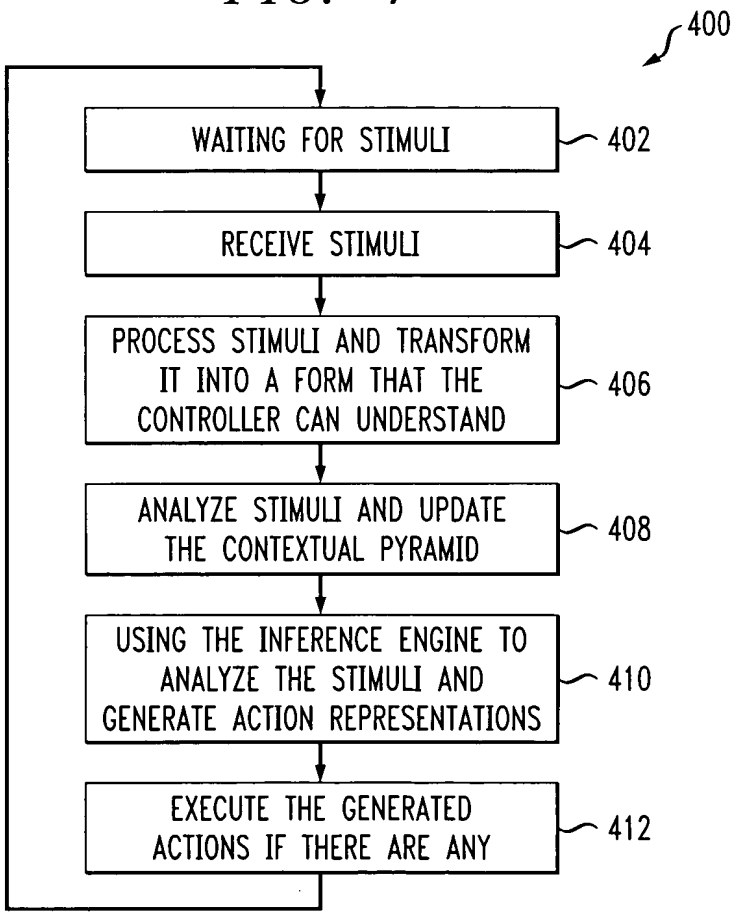


FIG. 5

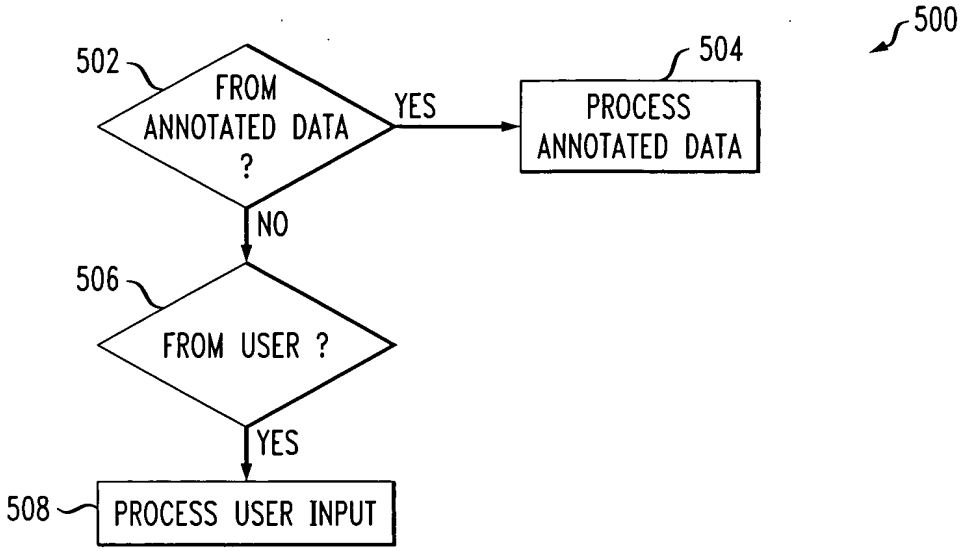


FIG. 6

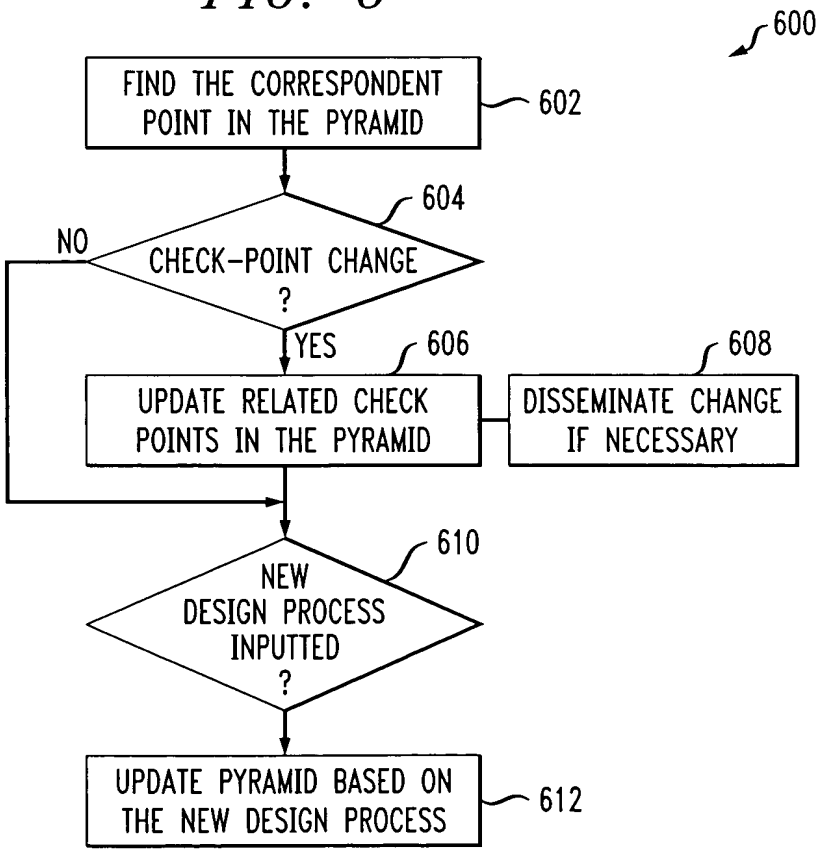
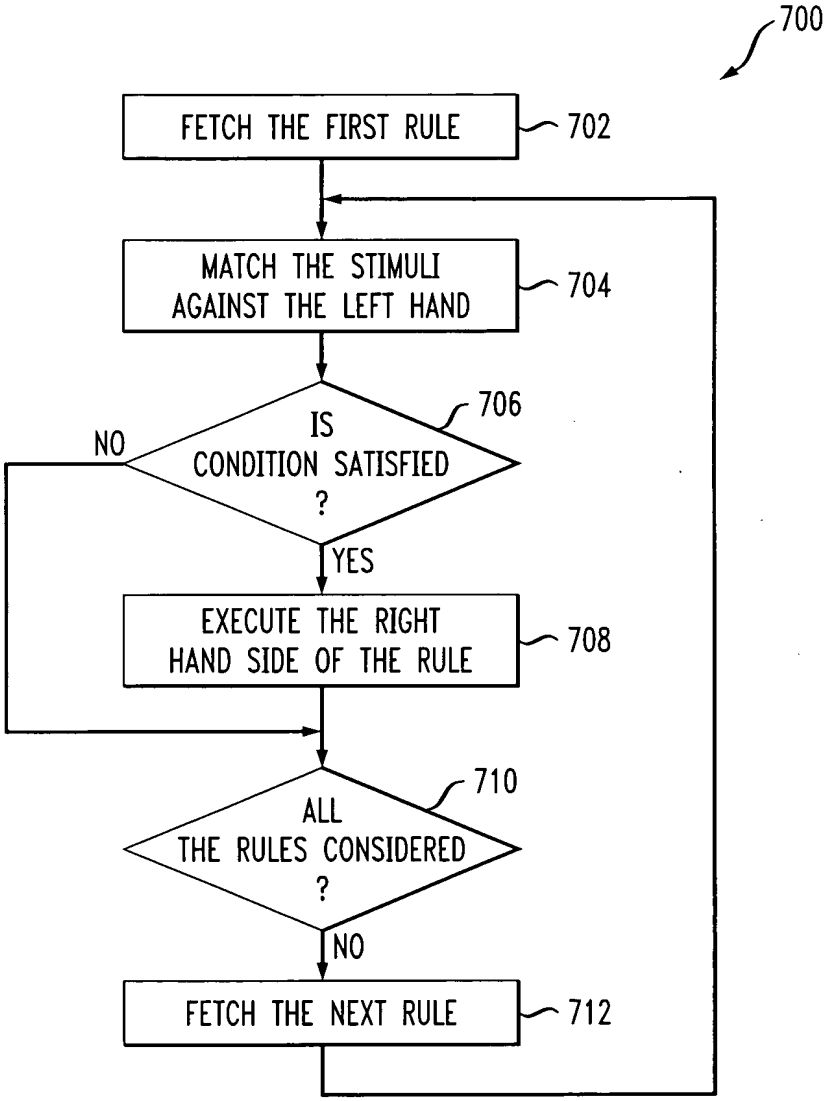


FIG. 7



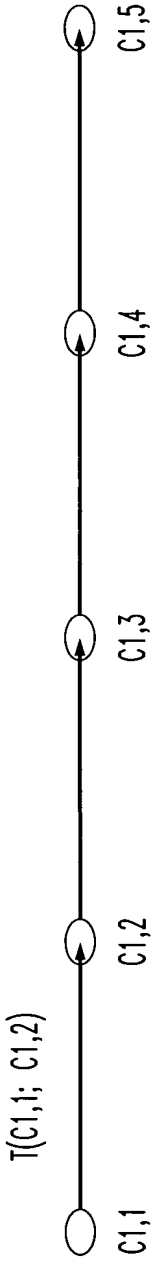


FIG. 8

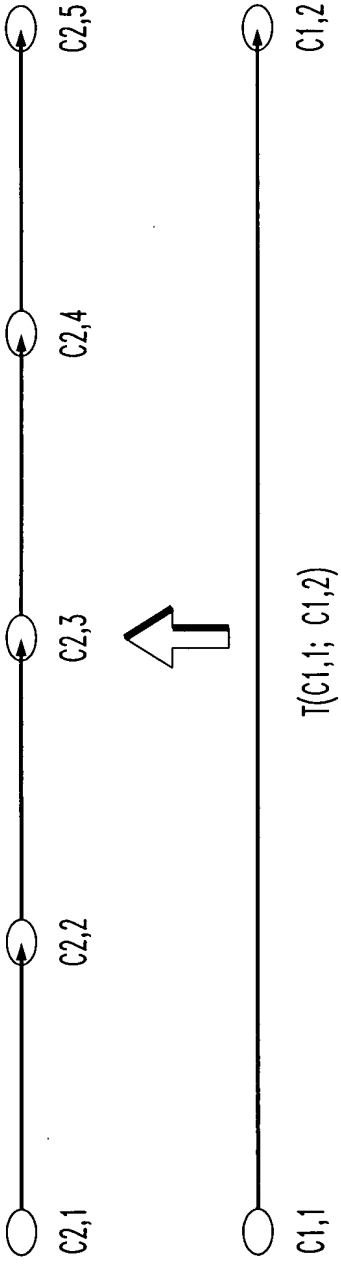


FIG. 9

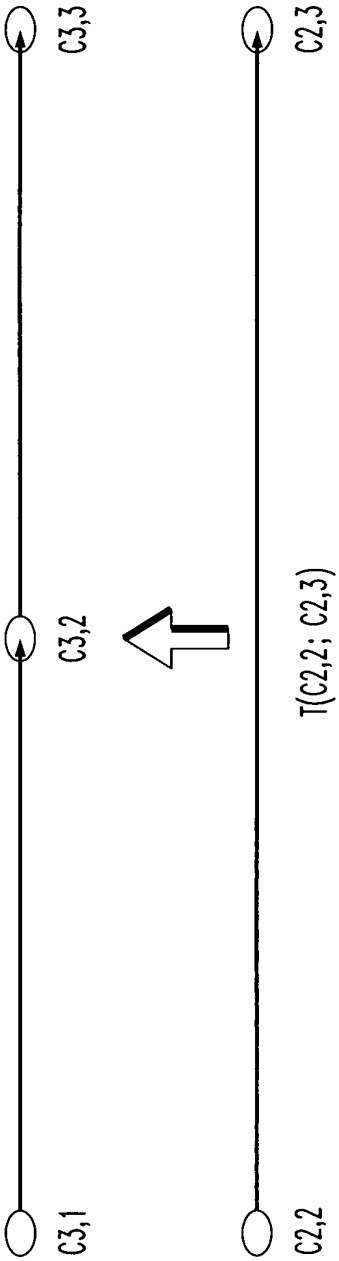


FIG. 10

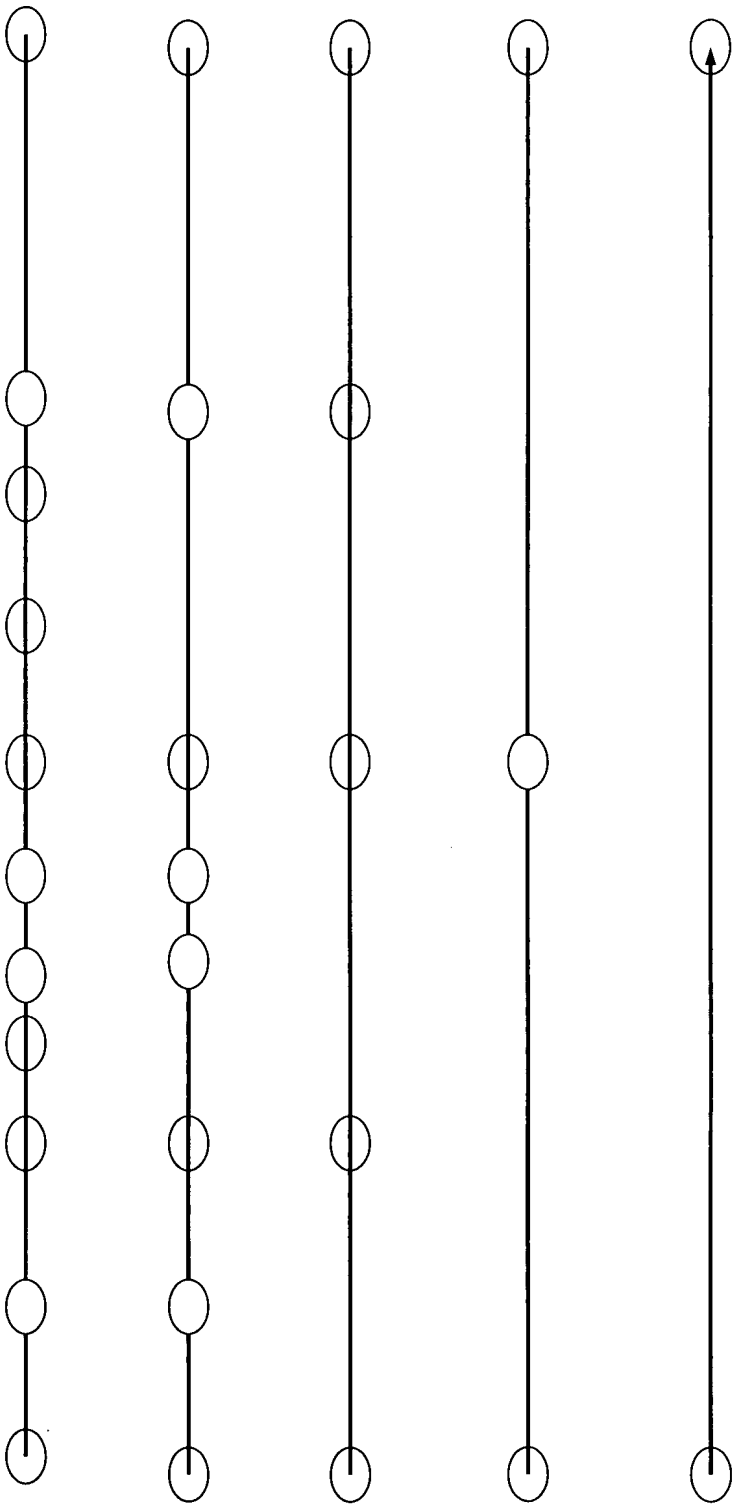


FIG. 11

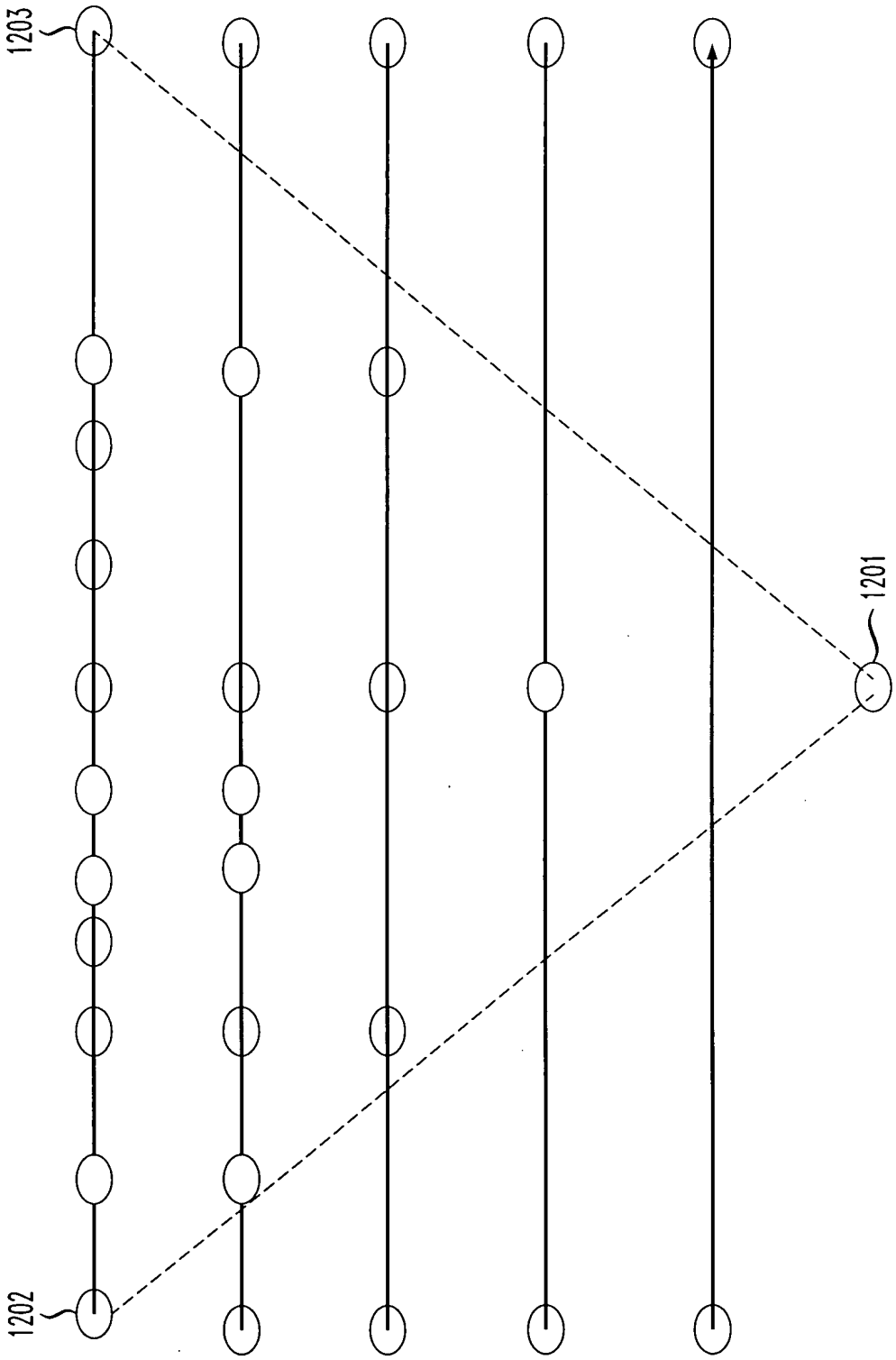


FIG. 12

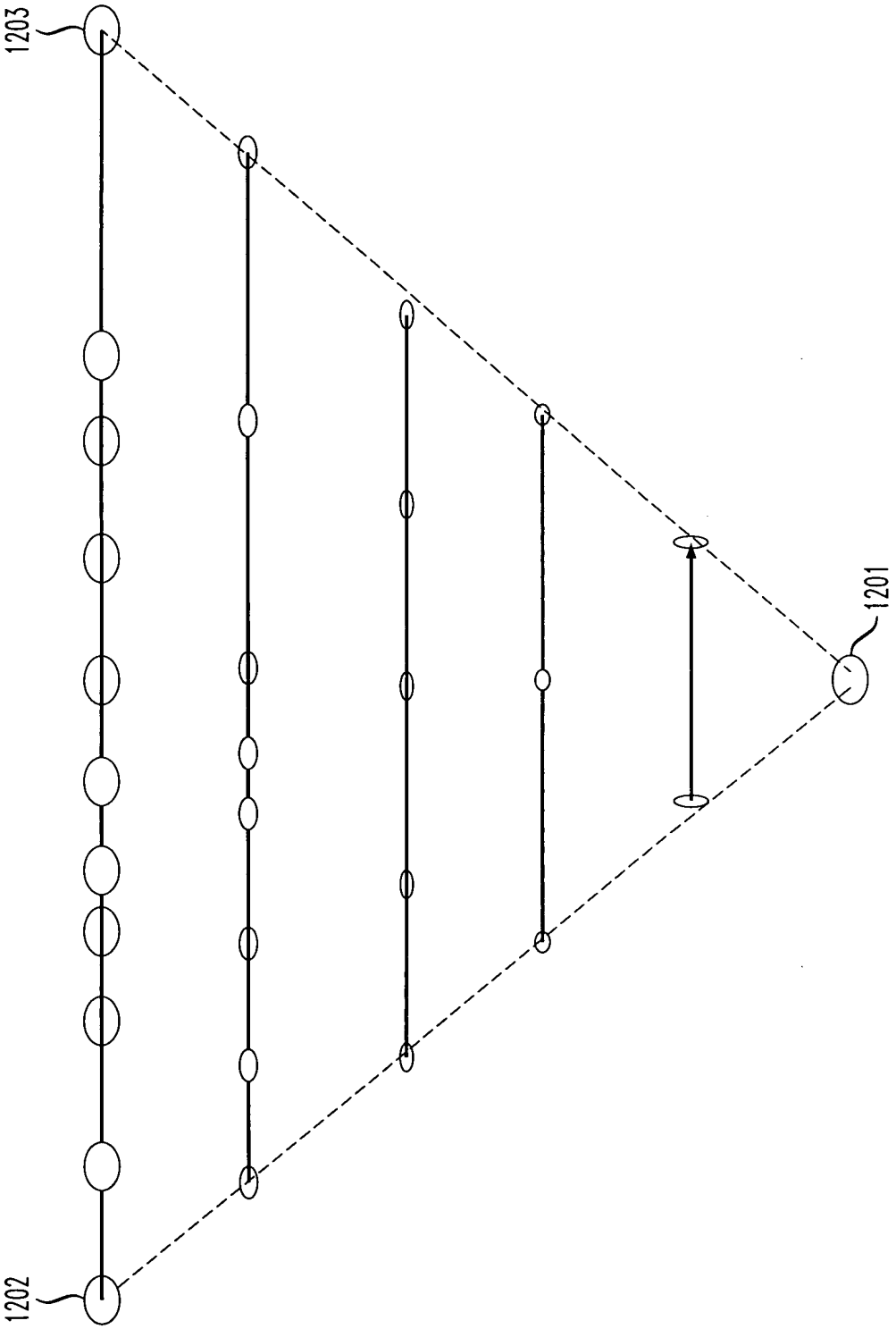


FIG. 13

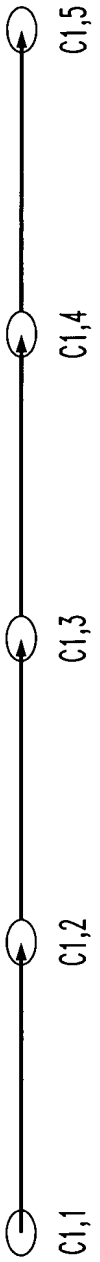


FIG. 14

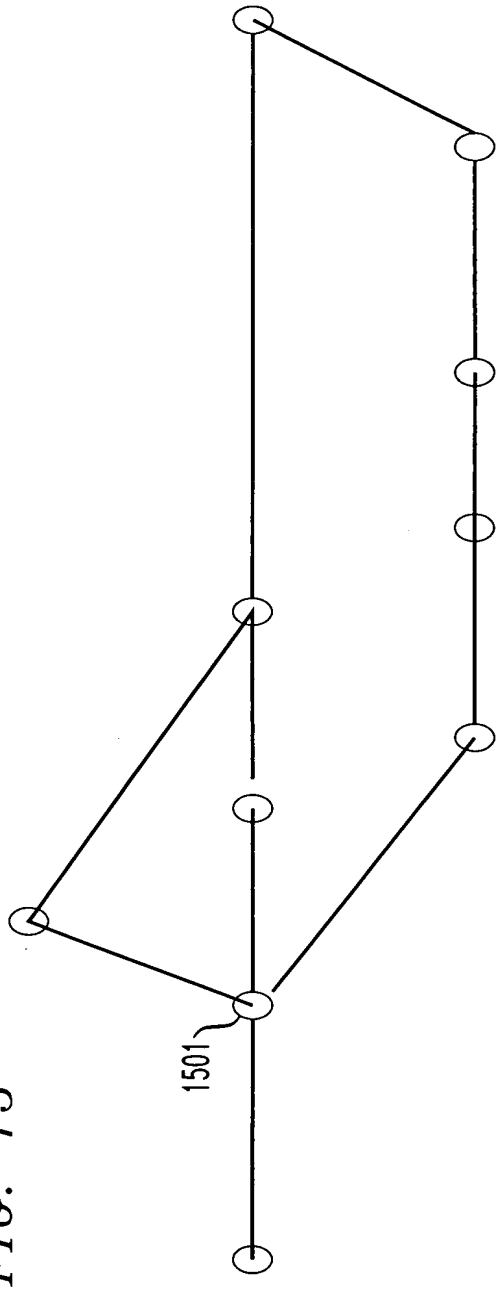


FIG. 15

FIG. 16

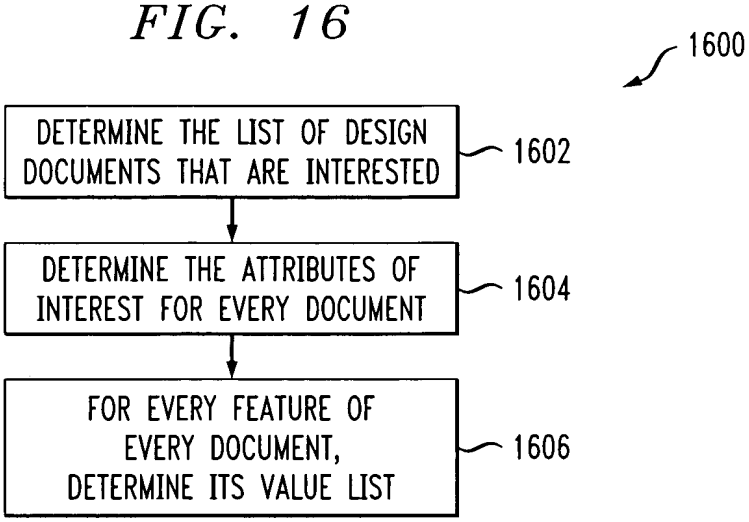


FIG. 17

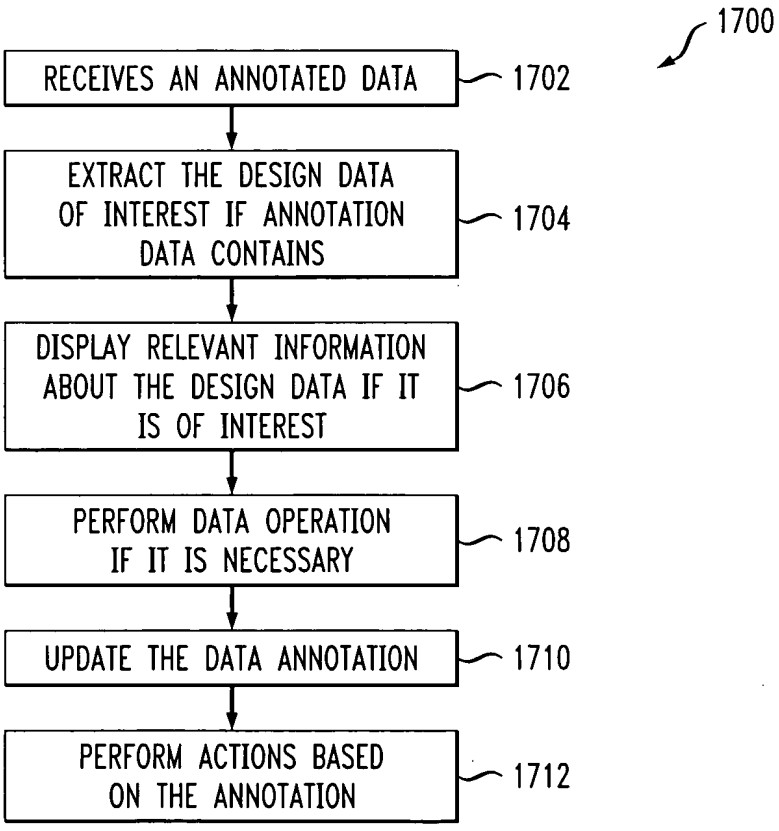


FIG. 18A

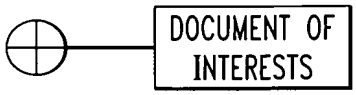


FIG. 18B

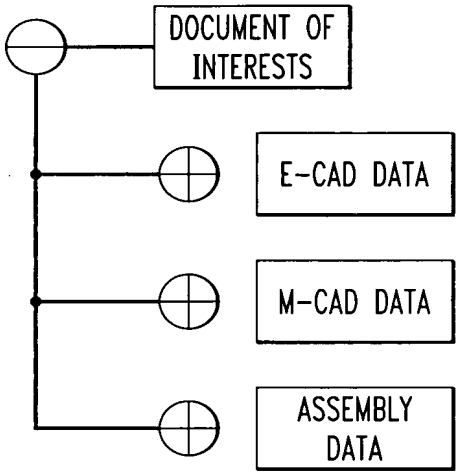


FIG. 18C

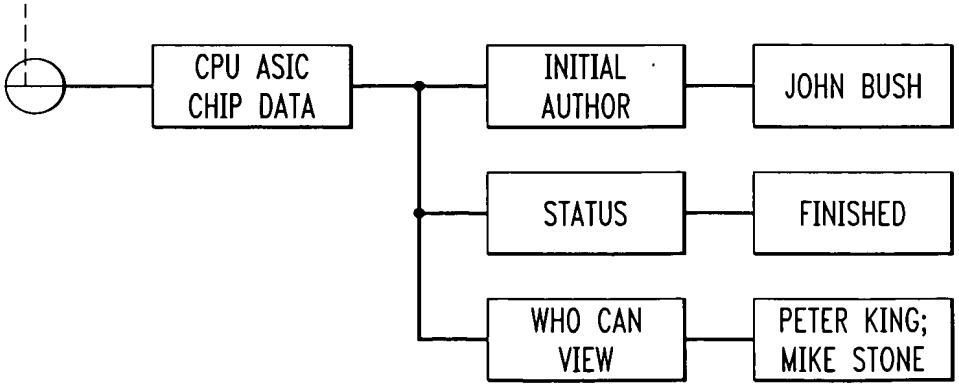
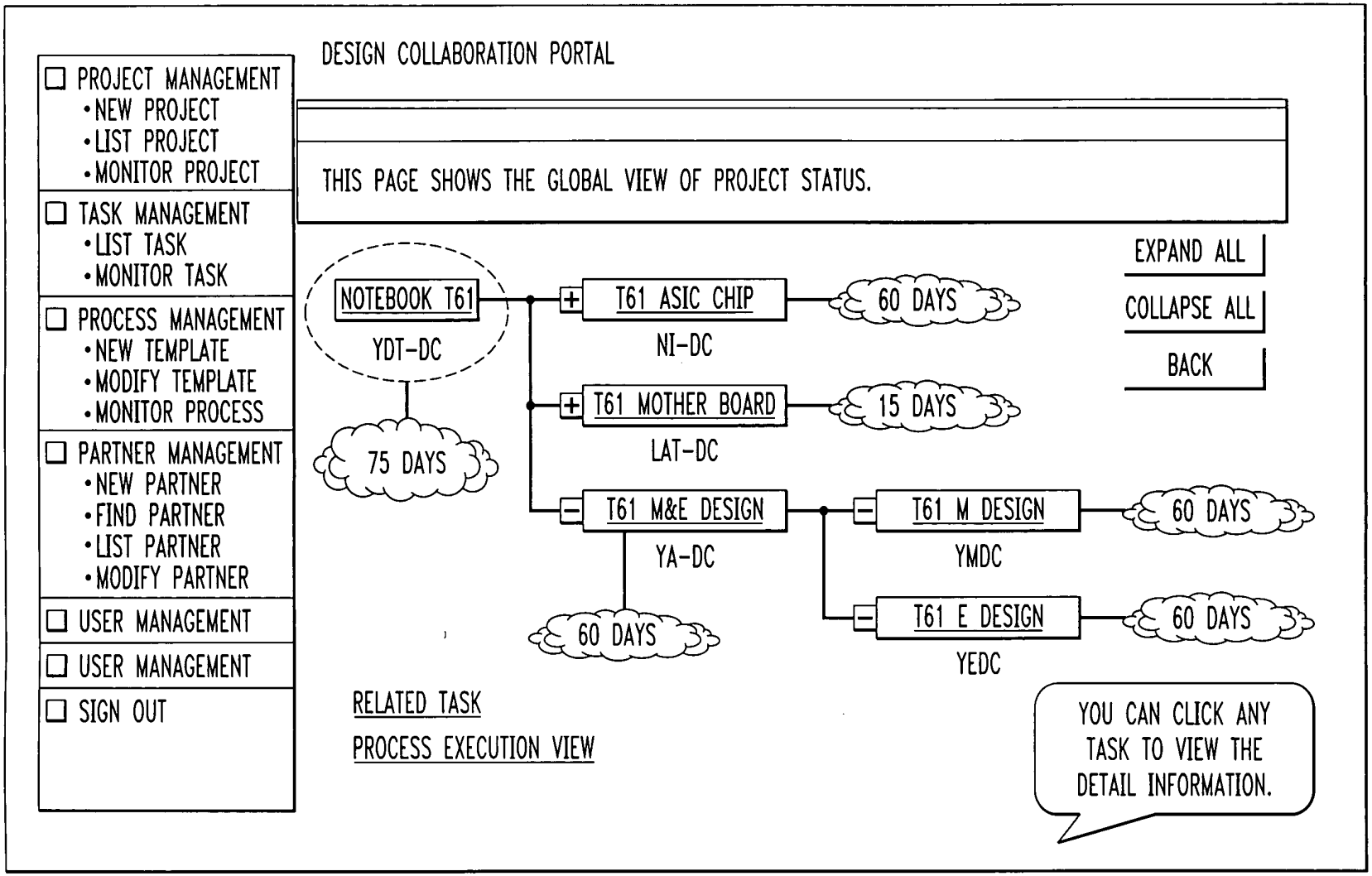


FIG. 19A

YDT-DC PROJECT VIEW



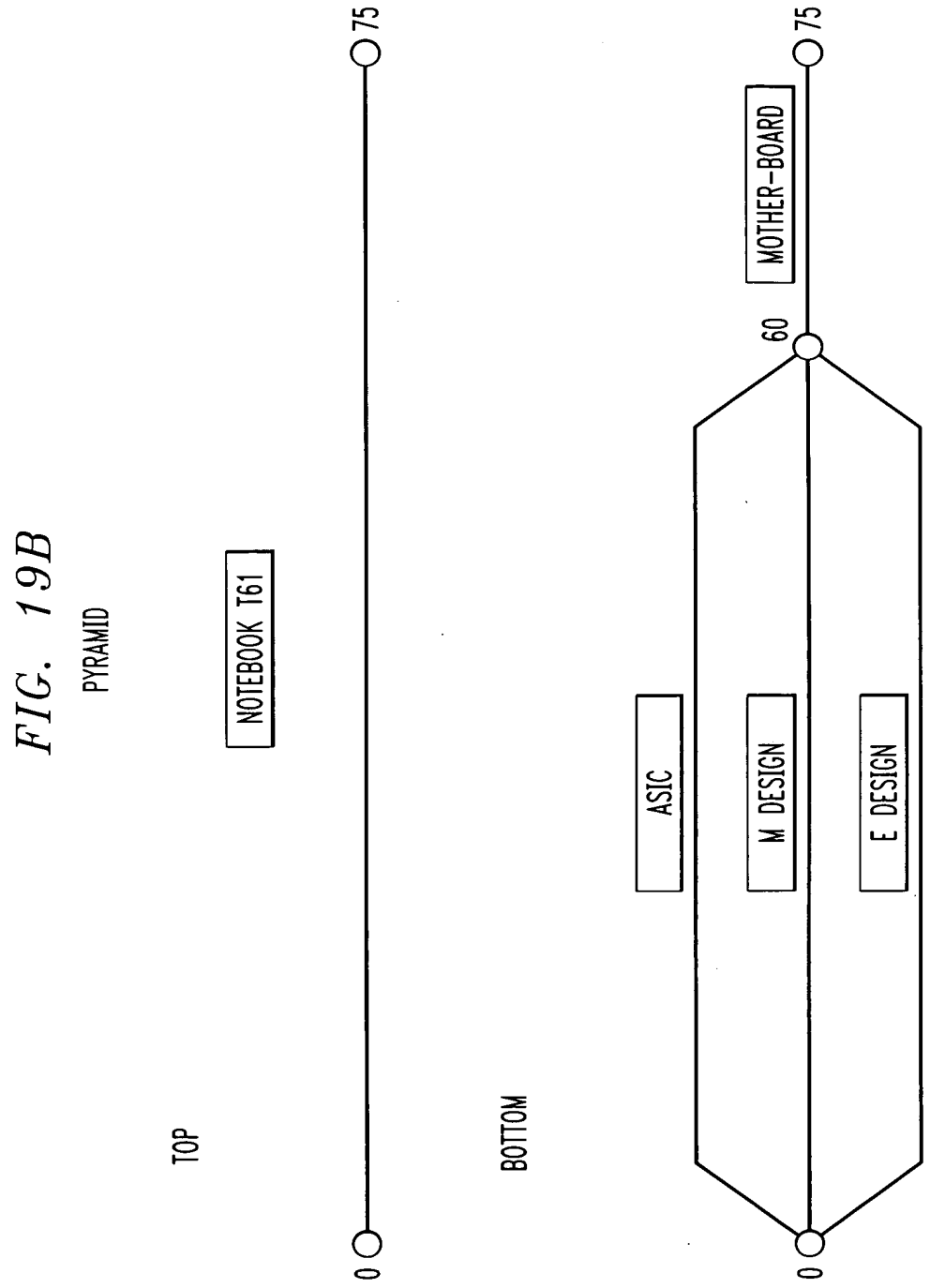


FIG. 19B

FIG. 19C

OFFSET CALCULATION

⊖ T61 M DESIGN YMDC	OFFSET:	T_ M Design - 60
⊖ T61 E DESIGN YEDC	OFFSET:	T_ E Design - 60
⊖ T61 M&E DESIGN YA-DC	OFFSET:	max { T_E Design - 60, T_M Design - 60}
⊕ T61 MOTHER BOARD LAT-DC	OFFSET:	max {T_E Design - 60, T_M Design - 60, T_ASIC - 60} + T_Board - 15
⊕ T61 ASIC CHIP NI-DC	OFFSET:	T_ ASIC - 60
NOTEBOOK T61 YDT-DC	OFFSET:	max {T_E Design - 60, T_M Design - 60, T_ASIC - 60} + T_Board - 15

IT MUST BE CALCULATED AFTER ALL M&E, ASIC
AT ANY TIME t, IF T_ASIC etc. WILL TAKE THE VALUE OF t FOR THE CALCULATION

FIG. 19D

CHECKPOINT CALCULATION

T61 M DESIGN YMDC	T_ M Design - 60 + 60
T61 E DESIGN YEDC	T_ E Design - 60 + 60
T61 M&E DESIGN YA-DC	max { T_E Design - 60, T_M Design - 60} + 60
T61 MOTHER BOARD LAT-DC	(T_Board - 15) + max{T_E Design - 60, T_M Design - 60, T_ASIC - 60} + 60
T61 ASIC CHIP NI-DC	OFFSET: T_ASIC - 60 + 60
NOTEBOOK T61 YDT-DC	SAME AS MOTHERBOARD IT MUST BE CALCULATED AFTER ALL M&E, ASIC

AT ANY TIME t, IF T_ASIC etc. WILL TAKE THE VALUE OF t FOR THE CALCULATION

FIG. 19E

ENERGY CALCULATION

0.5 * SIGN [CheckPoint - BaseCheckPoint]
* K
* [CheckPoint - BaseCheckPoint]^2

HERE K GIVES THE IMPORTANCE OF THE PROCESS

FIG. 20